

02/23/99

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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02/23/99

In re Patent Application of: Leonard Forbes et al.

Title: TRANSISTOR WITH VARIABLE ELECTRON AFFINITY GATE AND METHODS OF
FABRICATION AND USE

Attorney Docket No.: 303.324US2

PATENT APPLICATION TRANSMITTAL

BOX PATENT APPLICATION

Assistant Commissioner for Patents
Washington, D.C. 20231

We are transmitting herewith the following attached items and information (as indicated with an "X"):

DIVISIONAL of prior Patent Application No. 08/903,452 (under 37 CFR § 1.53(b)) comprising:

- Specification (32 pgs, including claims numbered 1 through 35 and a 1 page Abstract).
- Formal Drawing(s) (11 sheets).
- Copy of signed Declaration and Power of Attorney (6 pgs) from prior application.
- Incorporation by Reference: *The entire disclosure of the prior application, from which a copy of the oath or declaration is supplied herewith, is considered as being part of the disclosure of the accompanying application and is hereby incorporated by reference therein.*
- Check in the amount of \$760.00 to pay the filing fee.

Prior application is assigned of record to Micron Technology, Inc.

Information Disclosure Statement (1 pgs), Form 1449 (2 pgs). References NOT enclosed, cited in prior application.

Preliminary Amendment (1 pgs).

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The filing fee has been calculated below as follows:

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<input checked="" type="checkbox"/> MULTIPLE DEPENDENT CLAIMS PRESENTED				\$0.00
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By: Chris HammondSignature: Chris HammondDate of Deposit: February 23, 1999